

Prof. Gayatri MEHTA
University of North Texas, USA
Selected Scholarly Contributions SCOPUS

Mehta, G., Crawford, C., Luo, X., Parde, N., Patel, K., Rodgers, B., Sistla, A.K., Yadav, A., Reisner, M.
[Untangled: A game environment for discovery of creative mapping strategies](#)
(2013) ACM Transactions on Reconfigurable Technology and Systems, 6 (3), art. no. 13, .

Mehta, G., Luo, X., Parde, N., Patel, K., Rodgers, B., Sistla, A.K.
[UNTANGLED - An interactive mapping game for engineering education](#)
(2013) 2013 IEEE International Conference on Microelectronic Systems Education, MSE 2013, art. no. 6566700, pp. 40-43.

Yuan, X., Mehta, G., Jones, A.K.
[Guest editor's note interaction between compilers and computer architectures](#)
(2012) Journal of Circuits, Systems and Computers, 21 (2), art. no. 1202001, .

Gomathisankaran, M., Mehta, G., Namuduri, K.
[Power, performance and security optimized hardware design for H.264](#)
(2010) ACM International Conference Proceeding Series, art. no. 1852735, .

Mehta, G., Jones, A.K.
[An architectural space exploration tool for domain specific reconfigurable computing](#)
(2010) Proceedings of the 2010 IEEE International Symposium on Parallel and Distributed Processing, Workshops and Phd Forum, IPDPSW 2010, art. no. 5470735, .

Mehta, G., Stander, J., Baz, M., Hunsaker, B., Jones, A.K.
[Interconnect customization for a hardware fabric](#)
(2009) ACM Transactions on Design Automation of Electronic Systems, 14 (1), art. no. 11, .

Mehta, G., Ihrig, C.J., Jones, A.K.
[Reducing energy by exploring heterogeneity in a coarse-grain fabric](#)
(2008) IPDPS Miami 2008 - Proceedings of the 22nd IEEE International Parallel and Distributed Processing Symposium, Program and CD-ROM, art. no. 4536532, .

Mehta, G., Stander, J., Baz, M., Hunsaker, B., Jones, A.K.
[Interconnect customization for a coarse-grained reconfigurable fabric](#)
(2007) Proceedings - 21st International Parallel and Distributed Processing Symposium, IPDPS 2007; Abstracts and CD-ROM, art. no. 4228098, .

Mehta, G., Hoare, R.R., Stander, J., Jones, A.K.
[Design space exploration for low-power reconfigurable fabrics](#)
(2006) 20th International Parallel and Distributed Processing Symposium, IPDPS 2006, 2006, art. no. 1639484, .

Mehta, G., Hoare, R.R., Stander, J., Jones, A.K.
[A low-energy reconfigurable fabric for the SuperCISC architecture](#)
(2006) Proceedings - 14th Annual IEEE Symposium on Field-Programmable Custom Computing Machines, FCCM 2006, art. no. 4020934, pp. 309-310.

Jones, A.K., Hoare, R., Kusic, D., Stander, J., Mehta, G., Fazekas, J.
[A VLIW processor with hardware functions: Increasing performance while reducing power](#)
(2006) IEEE Transactions on Circuits and Systems II: Express Briefs, 53 (11), pp. 1250-1254.